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MEMC 98-3052(2512.2)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent of Robert J. Falster

Patent No. 6,849,901 *B2*

Issued February 1, 2005

Confirmation No. 7363

For A SILICON-ON-INSULATOR STRUCTURE HAVING A DEVICE LAYER WHICH  
IS VACANCY DOMINATED AND SUBSTANTIALLY FREE OF  
AGGLOMERATED VACANCY-TYPE DEFECTS

October 31, 2005

**REQUEST FOR EXPEDITED ISSUANCE**  
**OF CERTIFICATE OF CORRECTION UNDER 37 CFR 1.322**

TO THE COMMISSIONER FOR PATENTS,

SIR:

On studying the above-identified patent, the following errors, apparently made by the Patent and Trademark Office, were found (these errors are also noted on the attached form PTO-1050):

On the face of the patent, the title should read:

(54) -- SILICON-ON-INSULATOR STRUCTURE HAVING A DEVICE LAYER  
WHICH IS VACANCY DOMINATED AND SUBSTANTIALLY FREE OF  
AGGLOMERATED VACANCY-TYPE DEFECTS --.

*Certificate  
NOV 09 2005  
of Correction*

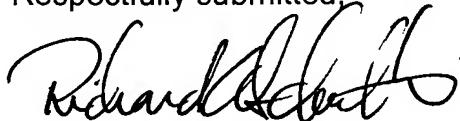
NOV 09 2005

REMARKS

In accordance with 37 CFR 1.322, a copy of a note from a September 9, 2004 telephone conference with Examiner Mai and a copy of a printout from PAIR dated October 29, 2005, are attached.

We respectfully request that a certificate of correction be issued.

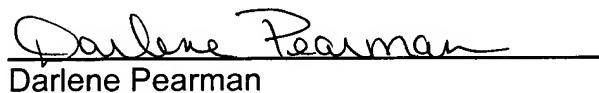
Respectfully submitted,



Richard A. Schuth, Reg. No. 47,929  
SENNIGER POWERS  
One Metropolitan Square, 16th Floor  
St. Louis, Missouri 63102  
(314) 231-5400

CERTIFICATE OF MAILING

I hereby certify that the foregoing Letter to the Patent and Trademark Office in the patent of Robert J. Falster, Patent No. 6,849,901, issued February 1, 2005 is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Mail Stop Post Issue, Certificate of Corrections Branch, Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450 on this 31st day of October, 2005.



Darlene Pearman  
Darlene Pearman

RAS/dep  
\*Enclosure

NOV 09 2005

UNITED STATES PATENT AND TRADEMARK OFFICE  
CERTIFICATE OF CORRECTIONPATENT NO. : 6,849,901 *B2*

DATED : February 1, 2005

INVENTOR(S): Robert J. Falster

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

On the face of the patent, the title should read:

(54) -- SILICON-ON-INSULATOR STRUCTURE HAVING A DEVICE LAYER WHICH IS VACANCY DOMINATED AND SUBSTANTIALLY FREE OF AGGLOMERATED VACANCY-TYPE DEFECTS --.

## MAILING ADDRESS OF SENDER:

PATENT NO. 6,849,901

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Senniger, Powers  
One Metropolitan Square, 16th Floor  
St. Louis, MO 63102

No. of additional copies



This collection of information is required by 37 CFR 1.322, 1.323, and 1.324. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 1.0 hour to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Attention Certificate of Corrections Branch, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

If you need assistance in completing the form, call 1-800-PTO-9199 and select option 2.

NOV 09 2005



9/9/04 - TC w/ Examiner Mai (MEMC 2512.2)

Title had been changed from SILICON ON INSULATOR STRUCTURE HAVING A LOW DEFECT DENSITY DEVICE LAYER AND A PROCESS FOR THE PREPARATION THEOF to A DEVICE LAYER OF A SILICON-ON-INSULATOR STRUCTURE HAVING VACANCY DOMINATED AND SUBSTANTIALLY FREE OF AGGLOMERATED VACANCY-TYPE DEFECTS **without a request from us to do so.**

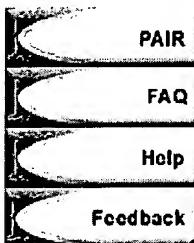
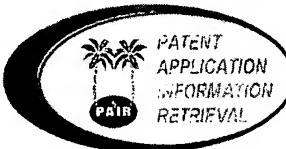
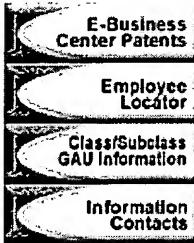
Asked Examiner Mai to change the title from his new title to A SILICON-ON-INSULATOR STRUCTURE HAVING A DEVICE LAYER WHICH IS VACANCY DOMINATED AND SUBSTANTIALLY FREE OF AGGLOMERATED VACANCY-TYPE DEFECTS.

Examiner Mai called back w/in 15 minutes to report that the title change had been made directly on their system and should be reflected on PAIR.

DER



## United States Patent and Trademark Office

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Search results as of: 10-29-2005::12:16

**Search results for patent number: 6,849,901**

Application Number:	10/038,084	Customer Number:	321
Filing or 371(c) Date:	01-03-2002	Status:	Patented Case
Application Type:	Utility	Status Date:	01-12-2005
Examiner Name:	MAI, ANH D	Location:	ELECTRONIC
Group Art Unit:	2814	Location Date:	-
Confirmation Number:	7363	Earliest Publication No:	US 2002-0113
Attorney Docket Number:	MEMC 98-3052 (2512.2)	Earliest Publication Date:	08-22-2002
Class/ Sub-Class:	257/347	Patent Number:	6,849,901
First Named Inventor:	Robert Falster, London, (GB)	Issue Date of Patent:	02-01-2005
Title Of Invention:	SOI structure having a device layer which is vacancy dominated substantially free from agglomerated vacancy type defects		

Select Search Option

 Assignments Continuity Data Display References Image File Wrapper Maintenance Fees - Retrieve fees to pay Maintenance Fees - View payment windows

Maintenance Fees Available: Mon-Fri 5:30 AM to Midnight, Sat-Sun-Hol. 7:30 AM to 8:00 PM E.T.

**File History**

Date	Contents Description
02-01-2005	Recordation of Patent Grant Mailed
01-12-2005	Issue Notification Mailed
02-01-2005	Patent Issue Date Used in PTA Calculation
01-07-2005	Receipt into Pubs
01-06-2005	Dispatch to FDC
01-06-2005	Application Is Considered Ready for Issue
10-21-2004	Issue Fee Payment Verified
12-22-2004	Receipt into Pubs
10-28-2004	Workflow - Query Request - Finish
10-21-2004	Issue Fee Payment Received
10-12-2004	Mail Miscellaneous Communication to Applicant
10-08-2004	Miscellaneous Communication to Applicant - No Action Count

10-08-2004	Workflow - Query Request - Begin
09-09-2004	Workflow - File Sent to Contractor
09-01-2004	Mail Notice of Allowance
09-01-2004	Mail Examiner Interview Summary (PTOL - 413)
08-31-2004	Issue Revision Completed
08-31-2004	Notice of Allowance Data Verification Completed
08-31-2004	Case Docketed to Examiner in GAU
08-27-2004	Examiner Interview Summary Record (PTOL - 413)
08-04-2004	Mail Paralegal TD Accepted
08-04-2004	Mail Advisory Action (PTOL - 303)
08-03-2004	Advisory Action (PTOL-303)
07-09-2004	Paralegal TD Accepted
06-28-2004	Terminal Disclaimer Filed
06-28-2004	Notice of Appeal Filed
07-08-2004	Date Forwarded to Examiner
06-28-2004	Amendment after Final Rejection
06-28-2004	Request for Extension of Time - Granted
06-28-2004	Workflow incoming amendment IFW
01-28-2004	Mail Notification of Terminal Disclaimer - Accepted
01-28-2004	Mail Notification of Terminal Disclaimer - Accepted
01-28-2004	Mail Final Rejection (PTOL - 326)
01-26-2004	Final Rejection
12-09-2003	Notification of Terminal Disclaimer - Accepted
11-07-2003	Terminal Disclaimer Filed
12-03-2003	Notification of Terminal Disclaimer - Accepted
11-07-2003	Terminal Disclaimer Filed
11-24-2003	IFW Amended case processing Complete
11-24-2003	Date Forwarded to Examiner
11-07-2003	Response after Non-Final Action
11-07-2003	Request for Extension of Time - Granted
11-24-2003	Reference capture on IDS
10-24-2003	Information Disclosure Statement (IDS) Filed
05-07-2003	Mail Non-Final Rejection
05-05-2003	Non-Final Rejection
04-25-2003	Date Forwarded to Examiner
04-17-2003	Response to Election / Restriction Filed
04-17-2003	Request for Extension of Time - Granted
03-06-2003	Mail Restriction Requirement
03-06-2003	Requirement for Restriction / Election
03-06-2003	Case Docketed to Examiner in GAU
02-27-2003	Case Docketed to Examiner in GAU
05-14-2002	Information Disclosure Statement (IDS) Filed
01-03-2002	Preliminary Amendment
04-29-2002	Case Docketed to Examiner in GAU
03-18-2002	Application Dispatched from OIPE
03-14-2002	Application Is Now Complete
03-08-2002	IFW Scan & PACR Auto Security Review
01-17-2002	IFW Scan & PACR Auto Security Review
01-03-2002	Initial Exam Team nn